Serial No.: 10/675,432   Docket No. 1001.29
Response to Office Action   Customer No. 53953

Amendments to the Specification

Please replace the paragraph beginning at page 16, line 3, with the following
rewritten paragraph.

—In response to the start of a next clock cycle $k+2$ ~~$k+1$~~, the latch 148 receives and
stores such information from the words W_{xyz}. Accordingly, via the control logic 100 and bus
116, the multiplexer 112 outputs (to the latch 148) either: (a) the words W_{x00} through W_{x07} if
the match is with Address Tag$_{x0}$, (b) the words W_{x10} through W_{x17} if the match is with
Address Tag$_{x1}$, (c) the words W_{x20} through W_{x27} if the match is with Address Tag$_{x2}$, or (d) the
words W_{x30} through W_{x37} if the match is with Address Tag$_{x3}$.—